

# BoT-nLE523D

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Dimension

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V 1.0.0

**History**

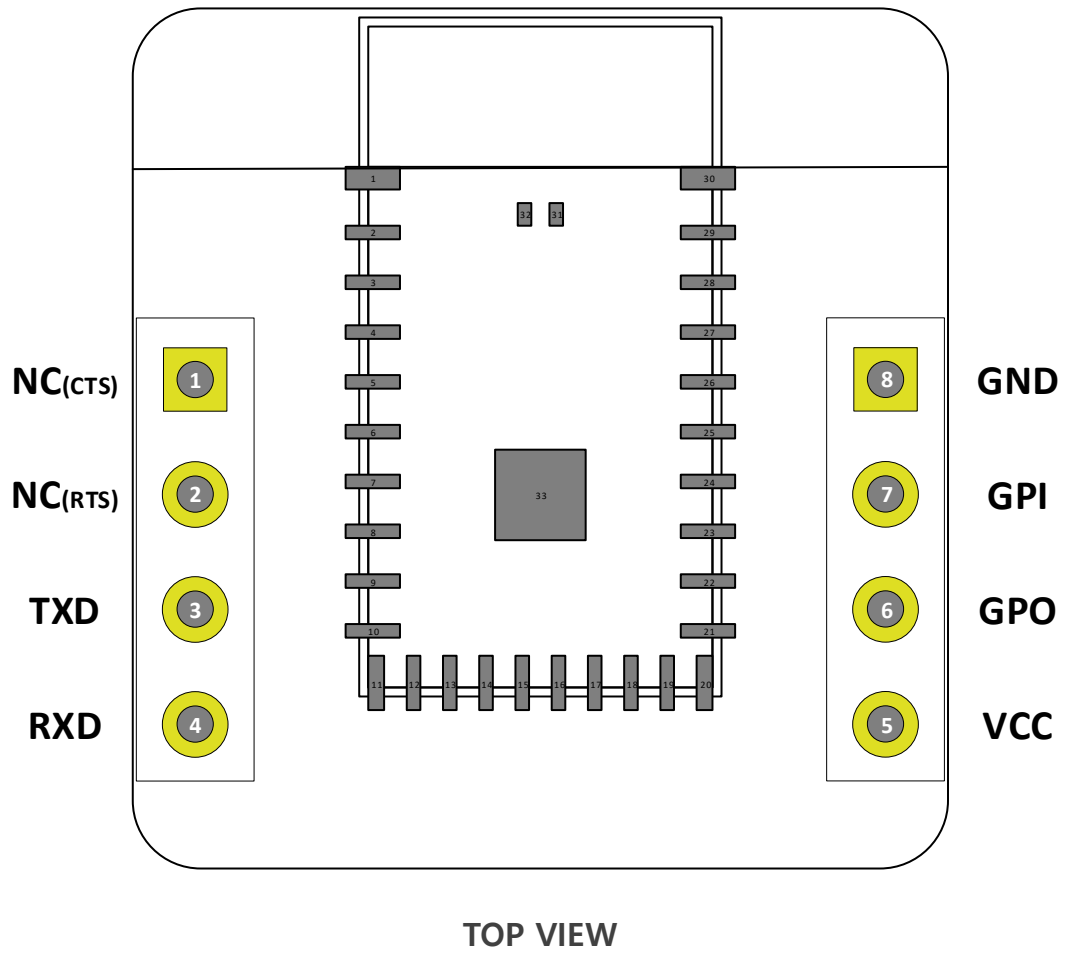
Rev	Date	Description	Author
1.0.0	2022. 09. 23	- First release	Enoch

## Table of Contents

1. Pin configuration & PIN Description .....	4
1.1 Pin Configuration .....	4
1.2 PIN Description .....	5
1.3 DC Characteristics .....	5
1.4 Dimensions .....	6
2. Application Schematic .....	7
2.1 Reference Application .....	7

1. Pin configuration & PIN Description

1.1 Pin Configuration



## 1.2 PIN Description

Pin No.	Pin Name	Pin Function	Description
1	NC(CTS)	UART CTS	UART Clear to Send (default disable)
2	NC(RTS)	UART RTS	UART Request to Send (default disable)
3	TXD	UART TXD	UART Transmit Data
4	RXD	UART RXD	UART Receive Data
5	VCC	POWER	Main Power. typ. DC 3.3V
6	GPO (STATUS)	Connection Status	Connection status; Connected Device = High Disconnected Device = Low
7	GPI (AT COMMAND)	AT COMMAND	AT COMMAND MODE control; AT COMMAND MODE = High BYPASS MODE = Low
8	GND	GROUND	GROUND

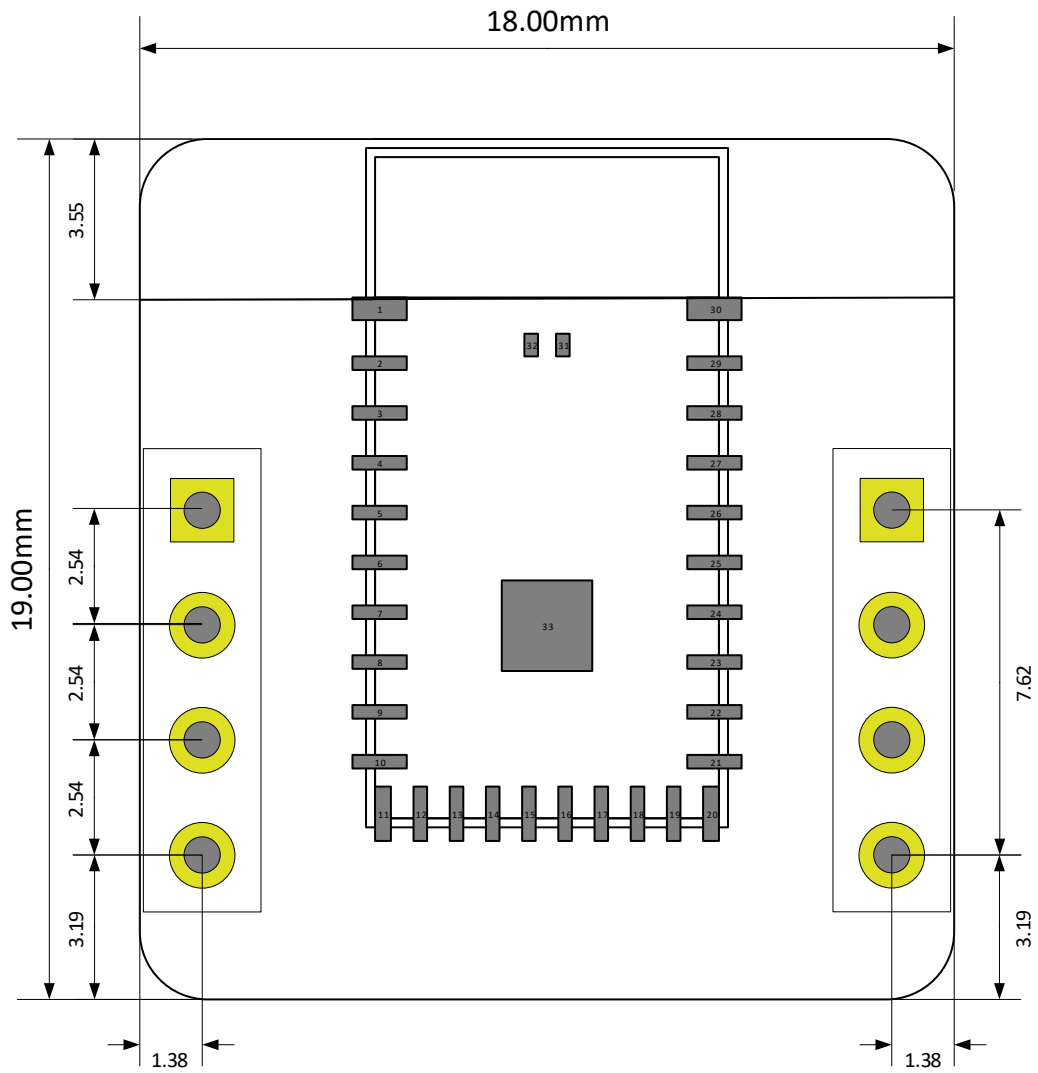
1) ALL I/O function operate on CHIPSEN commercial firmware.

2) For more information refer to CHIPSEN commercial firmware "AT COMMAND User Manual" document.

## 1.3 DC Characteristics

Symbol	Parameter (condition)	Min.	Typ.	Max.	Units
VDD	Main Power	2.7	3.3	3.6	V
V <sub>IH</sub>	Input high voltage	0.7 X VDD		VDD	V
V <sub>IL</sub>	Input low voltage	VSS		0.3 X VDD	V
V <sub>OH,HDH</sub>	Output high voltage, high drive, 5 mA, VDD ≥ 2.7 V	VDD-0.4		VDD	V
V <sub>OL,HDH</sub>	Output low voltage, high drive, 5 mA, VDD ≥ 2.7 V	VSS		VSS +0.4	V
R <sub>PU</sub>	Internal Pull-up resistance	11	13	16	kΩ
R <sub>PD</sub>	Internal Pull-down resistance	11	13	16	kΩ

### 1.4 Dimensions



TOP VIEW

## 2. Application Schematic

### Design consideration

- All I/O(including UART) should be up after VCC applied.
- All I/O(including UART) should NOT be present fast or be held high before VCC is high.

### 2.1 Reference Application

## ■ BoT-nLE523D REF APPLICATION

